	Туре	1 #	Hits	Search Text	DBs	Time Stamp	Comments
	BRS	L1	6534	(input or ingress) and (processor or controller or engine) and ((load same balanc\$3) or (round same robin)) and (cell or packet) and rout\$3	US- PGPUB; USPAT	2005/08/01	
8	BRS	L2	68	1 and spray\$3	US- PGPUB; USPAT	2005/08/01 10:32	

	Туре	# 1	Hits	Search Text	DBs	Time Stamp	Comments
н	BRS	L.1	0	(pural same input same ports) and (parallel same processing) and rout\$3 and US-(packet or cell) and ((load PGPUB; same balancing) or (round USPAT same robin)) and (engine or processor)		2005/08/01 12:30	
~ ~	BRS	L2	13	(plural same input same ports) and (parallel same processing) and rout\$3 and US-(packet or cell) and ((load PGPUB; same balancing) or (round USPAT same robin)) and (engine or processor)		2005/08/01 12:30	